

Amendments to the Claims

The listing of claims will replace the previous version, and the listing of claims:

Listing of Claims

1. (currently amended) A control device for controlling an electronic device, comprising:

~~connecting means for connecting~~ a socket to be connected to an external memory,

non-volatile storage memory means having a memory region with a portion where data can be electrically rewritten, said memory means having a first memory region for storing control program data for operating the electronic device, a second memory region for storing connection judgment program data to determine whether the external memory means is connected to the ~~connecting means~~ socket, and a third memory region for storing writing program data to rewrite the control program data stored in the first memory region, and

control means electrically connected to the ~~connecting means~~ socket and memory means for executing a control program to control an operation of the electronic device, said control means initially executing the connection judgment program data, and when it is determined that the external memory ~~medium~~ is connected to the socket ~~control device~~, the control means executing the writing program data so that at least a portion of the control program data stored in the first memory region of the memory means is rewritten ~~based on~~ to external memory medium data stored in the external memory ~~medium~~.

2. (currently amended) A control device according to claim 1, wherein said control means executes the writing program when data stored in the external memory ~~medium~~ at a predetermined address

thereof is read through executing the connection judgment program and the data matches predetermined data stored in the memory means.

3. (currently amended) A control device according to claim 1, wherein said control means executes operation control program based on the operation control program data stored in the first memory region when data stored in the external memory ~~medium~~ at a predetermined address thereof is read through executing the connection judgment program and the data does not match predetermined data stored in the memory means.

4. (original) A control device according to claim 1, wherein said control means executes the connection judgment program when the control device is turned on.

5. (currently amended) A control device according to claim 1, wherein said memory means further includes a fourth memory region for storing compatibility judgment program data for determining whether the external memory medium data stored in the external memory means is compatible and correct, said control means executing the writing program so that at least a portion of the control program data stored in the first memory region in the memory means is rewritten based on the external memory medium data stored in the external memory medium when it is determined that the external memory ~~medium~~ is connected to the control device through executing the connection judgment program and that the external memory medium data stored in the external memory medium is correct through executing the compatibility judgment program.

6. (currently amended) A control device according to claim 5, wherein said control means outputs an error signal when it is determined that the external memory medium data stored in the

external memory ~~medium~~ is incorrect through executing the compatibility judgment program.

7-12. (canceled)

13. (new) An automatic document feeder comprising: feeding means for feeding original onto a platen, discharging means for discharging the original on the platen, and a control board for controlling the feeding means and the discharging means, said control board including the control device according to claim 1 and executing the connection judgment program when the control device is turned on.

14. (new) A control device for controlling an operation of an electronic device, comprising:

- a single chip computer having an EEPROM capable of electrically rewriting data therein,

- a control board having said single chip computer thereon,

- a socket mounted on the control board, to which an EPROM having a memory area with predetermined addresses assigned thereto is attached,

- a bus for connecting the single chip computer and the EPROM through the socket,

- rewriting means memorized in a boost area of the EEPROM and writing data memorized in the EPROM to a predetermined area of the EEPROM,

- judgment means memorized in the boost area of the EEPROM and judging whether the EPROM is connected to the socket, and control means for executing the judgment means when the control device is turned on, said control means executing the rewriting means when it is judged that the EPROM is connected to the socket.